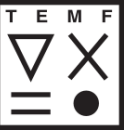


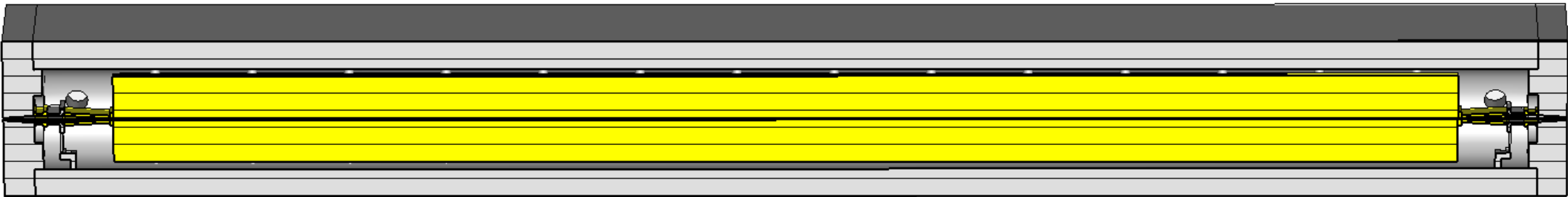
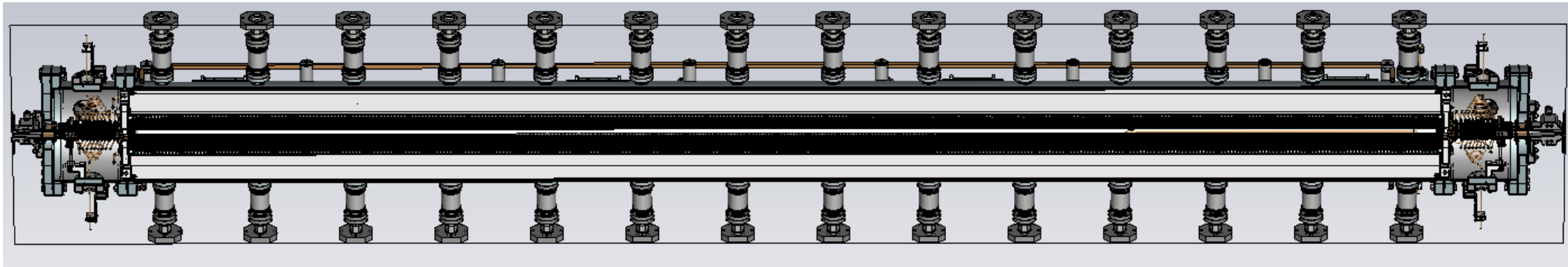
SHUNT IMPEDANCE SIMULATIONS FOR AN IVU AT PETRA-4



CONTENT

- Geometry
- Simulation procedure
- Results
- Summary and outlook

GEOMETRY

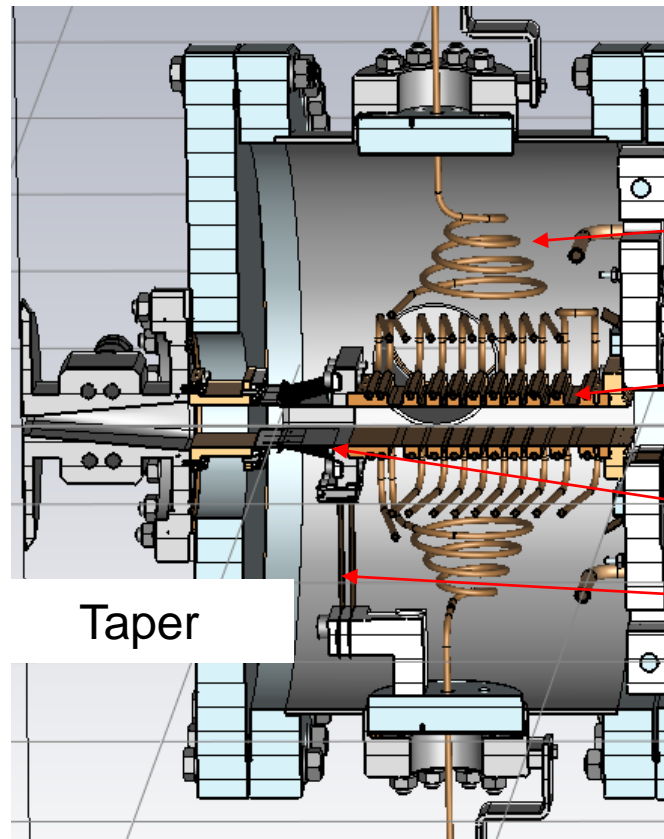
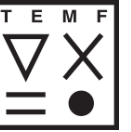


Entry

Grey: stainless steel $1.4e6$ S/m Body
Yellow: PEC

Exit

GEOMETRY



Coolant pipes

Flexible taper

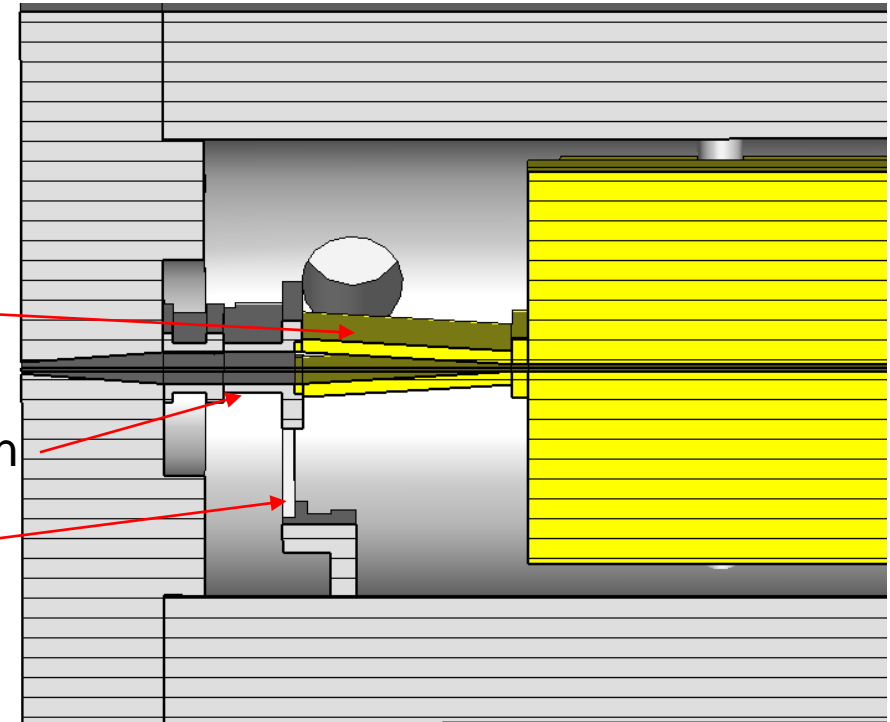
Sliding connection

Springs

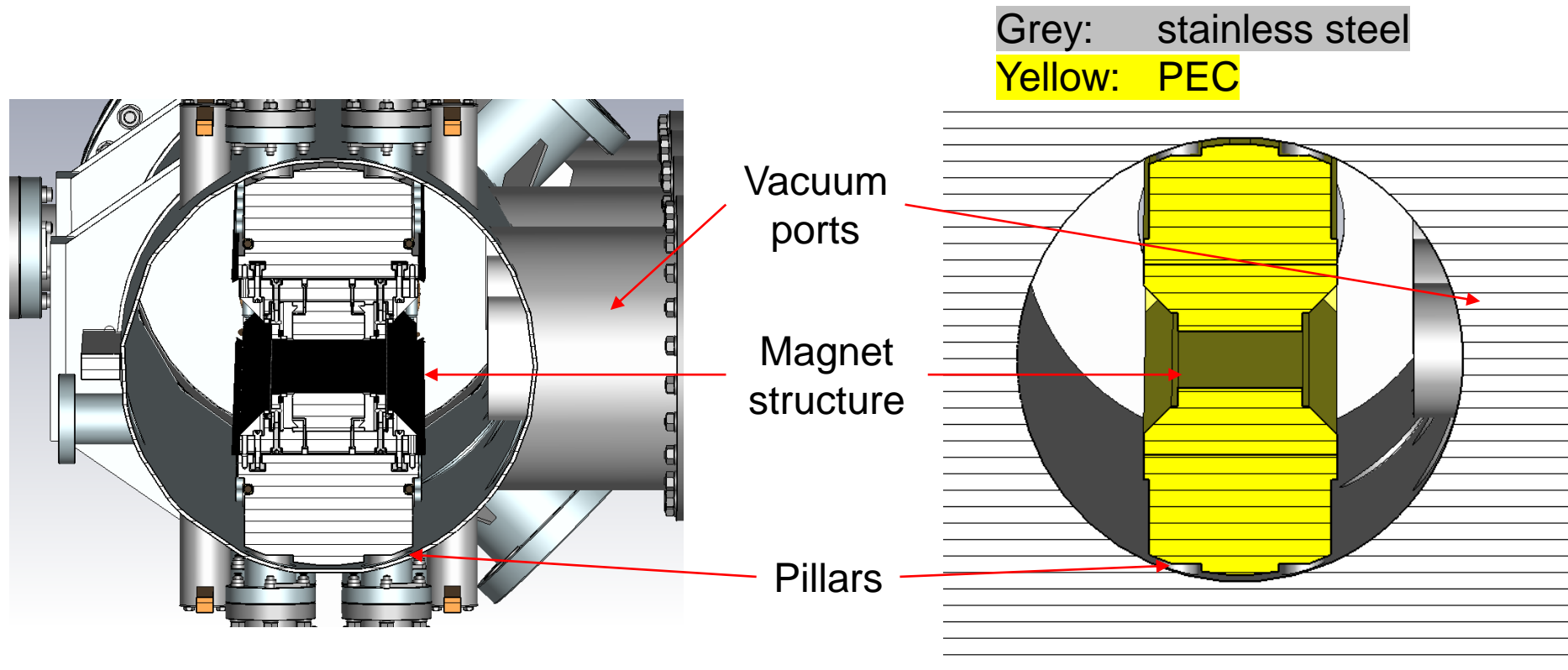
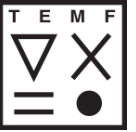
Taper

Grey: stainless steel

Yellow: PEC

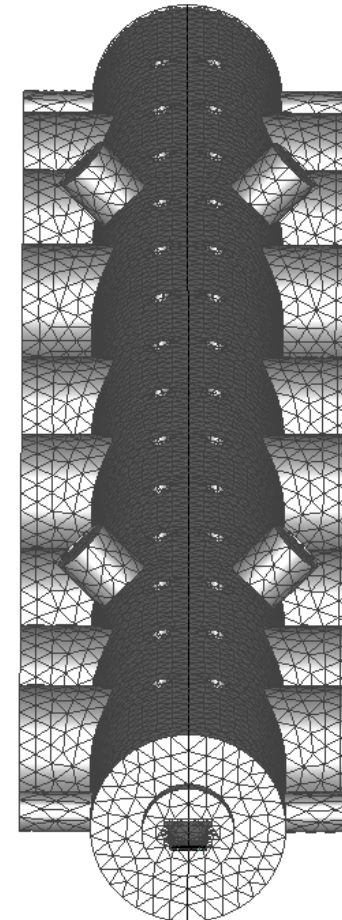


GEOMETRY



SIMULATION PROCEDURE

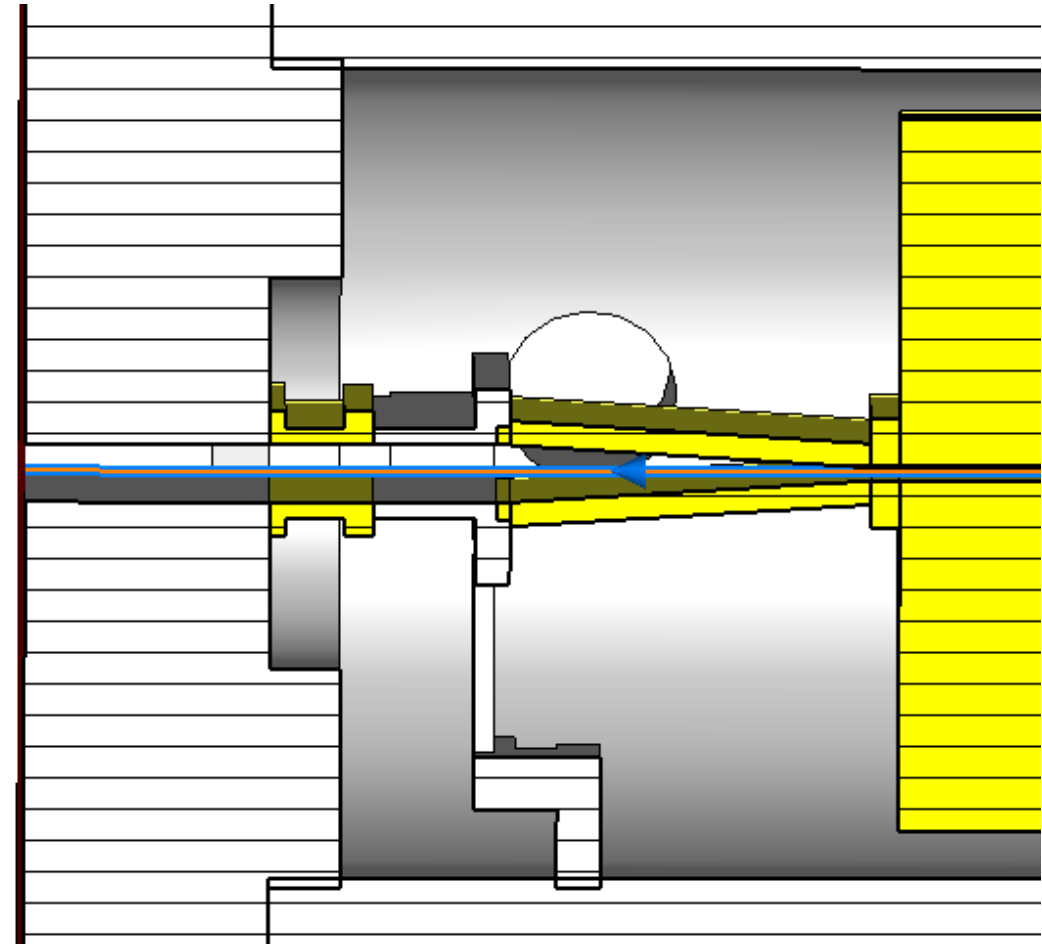
- Three independent solver
 - CST wakefield
 - CST eigenmode
 - FELIS (frequency domain impedance)
- Centered beam does not excite modes
 - Offset in horizontal direction has no impact
 - Small displacement in vertical direction (0.5mm)
- Model almost symmetric
 - Neglect cooling pipes
 - Mirror vacuum ports
 - Neglect small asymmetry in exit geometry

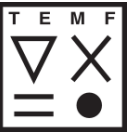


SIMULATION PROCEDURE

CST Wakefield Solver

- Geometry approximation
 - Entry taper simplified
 - Cutoff frequency ≈ 2 GHz
 - Mesh
 - Tapers
 - Tank
- Wake potential \rightarrow impedance
 - \cos^2 window
 - DFT
- Limitation: runtime





SIMULATION PROCEDURE

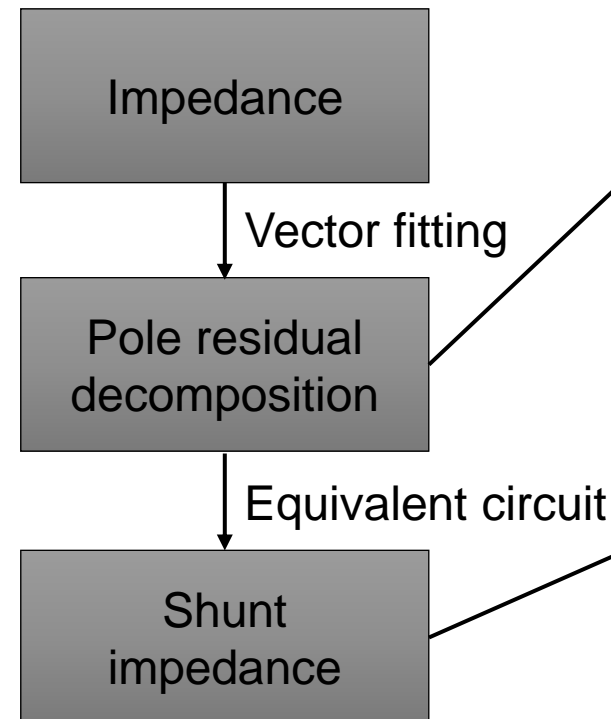
FELIS

- Impedance Solver in FD + FFS
- Geometry approximation
 - No additional simplification at entry/exit
 - 2nd order elements
- High order basis functions
- Materials
 - Only one surface impedance due to FFS
- Limitation: memory, (runtime)

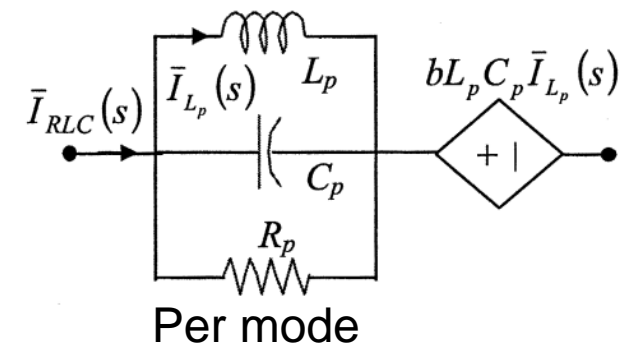
SIMULATION PROCEDURE

FELIS

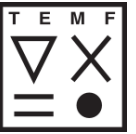
- Impedance Solver in FD + FFS
- Geometry approximation
 - No additional simplification at entry/exit
 - 2nd order elements
- High order basis functions
- Materials
 - Only one surface impedance due to FFS
- Limitation: memory, (runtime)



$$Z(\sigma) = d + \sigma e + \sum_{k=1}^K \frac{c_k}{\sigma - p_k}$$



Giulio Antonini, *SPICE*
Equivalent Circuits of
Frequency-Domain Responses



SIMULATION PROCEDURE

CST Eigenmode Solver

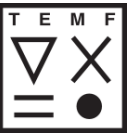
- No modes propagating in the beam pipes
 - Direct integration

$$V_i = \int_a^b E_{z,i}(z) e^{j\omega_i z/c_0} dz$$

- Longitudinal shunt impedances

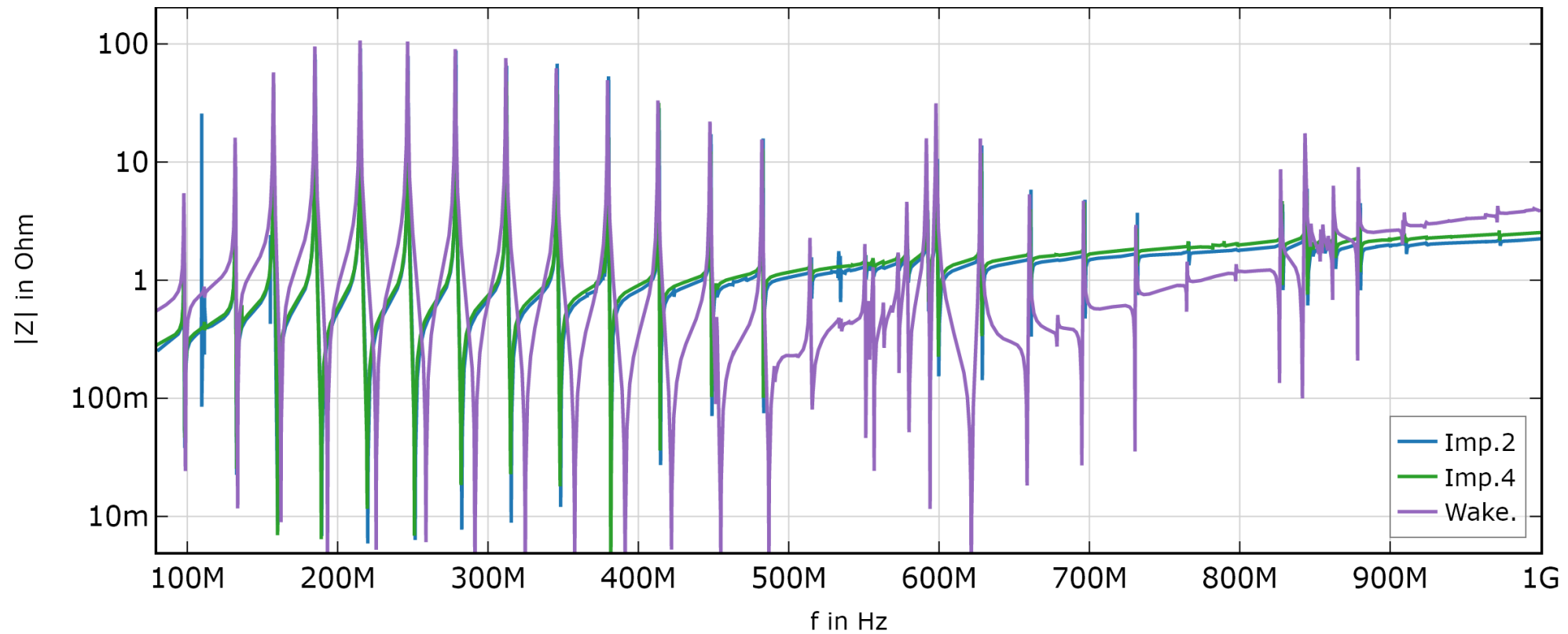
$$R_{s,i} = \frac{|V_i|^2 Q_i}{2W_i \omega_i}$$

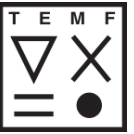
- Limitation: memory



RESULTS

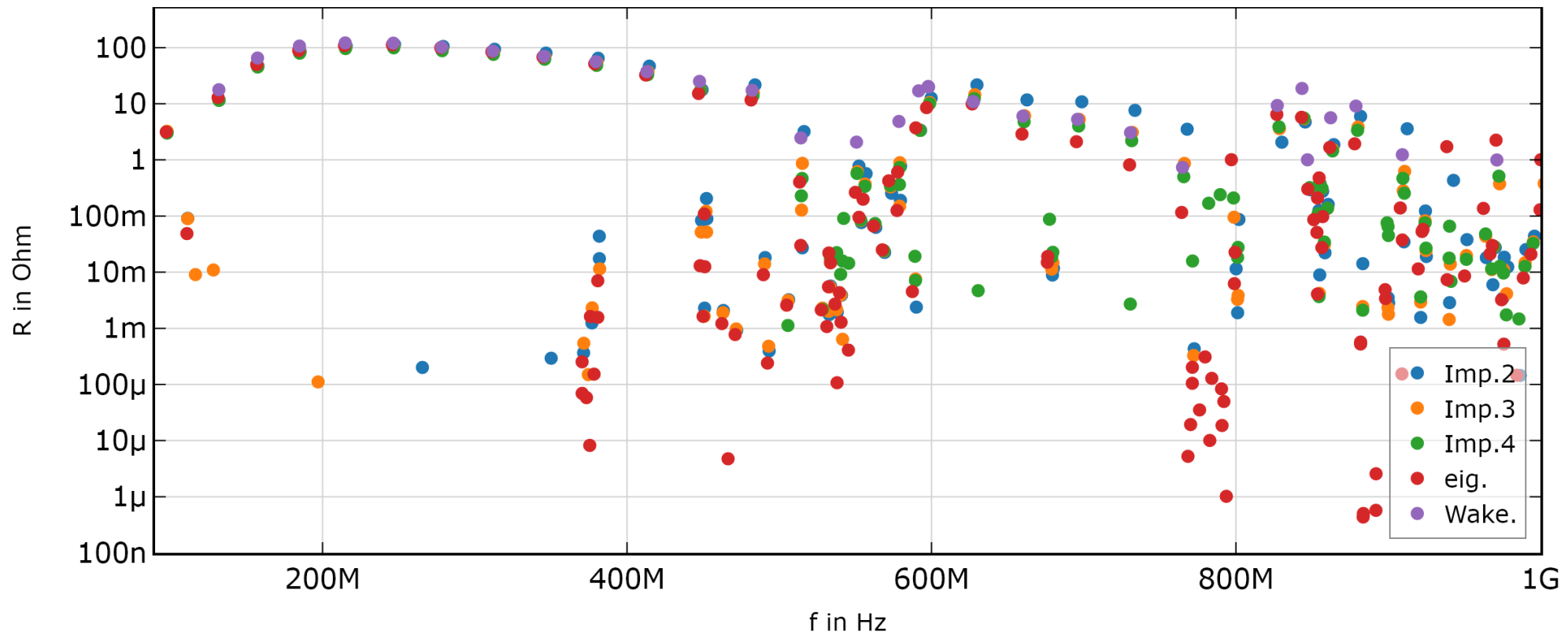
Impedances Comparison (PEC + steel)





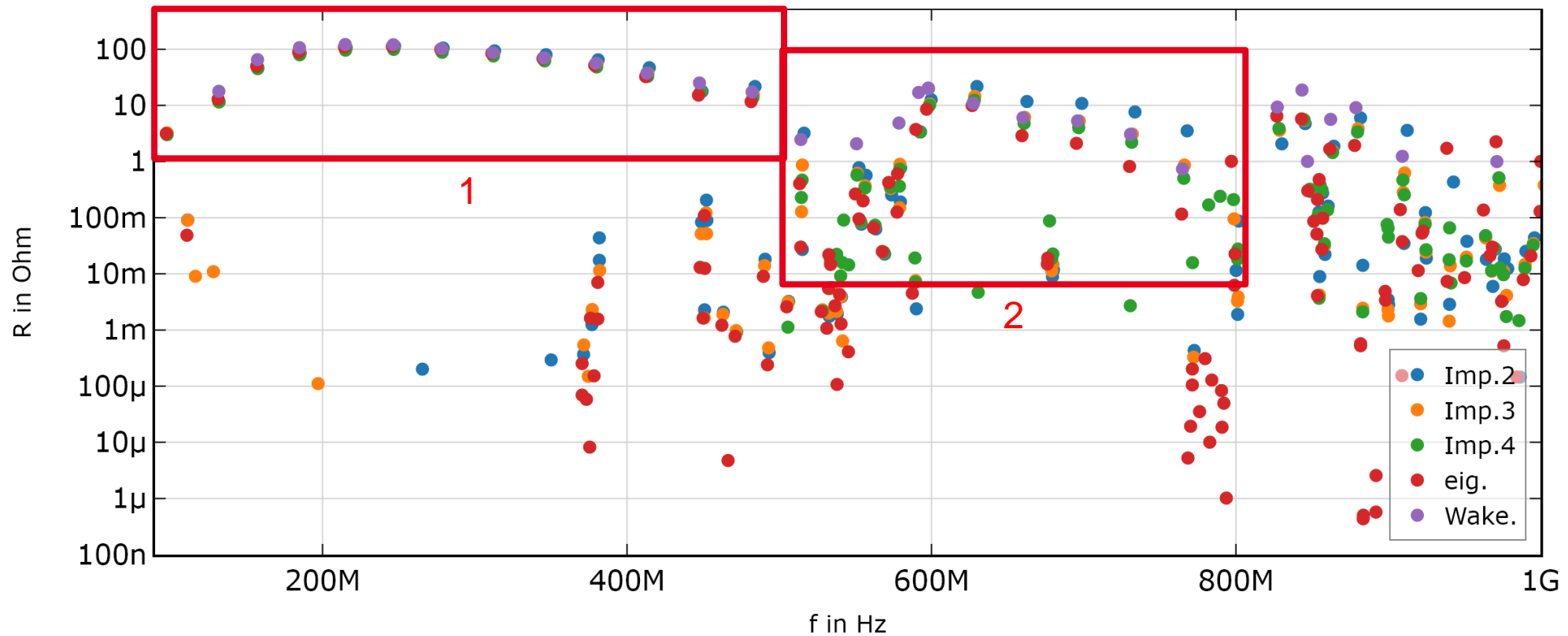
RESULTS

Shunt Impedances Comparison (PEC + steel)



RESULTS

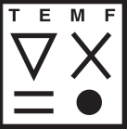
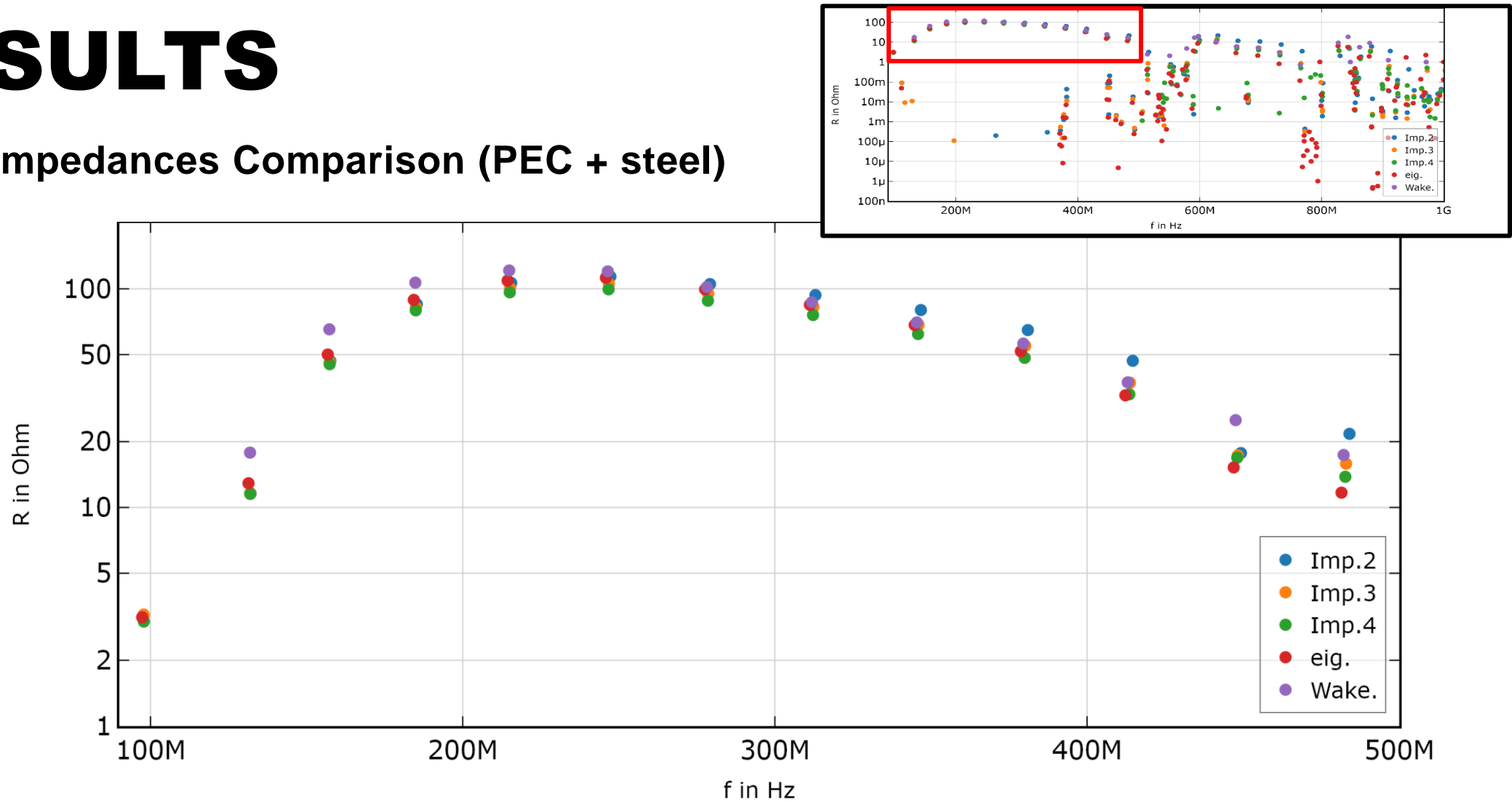
Shunt Impedances Comparison (PEC + steel)





RESULTS

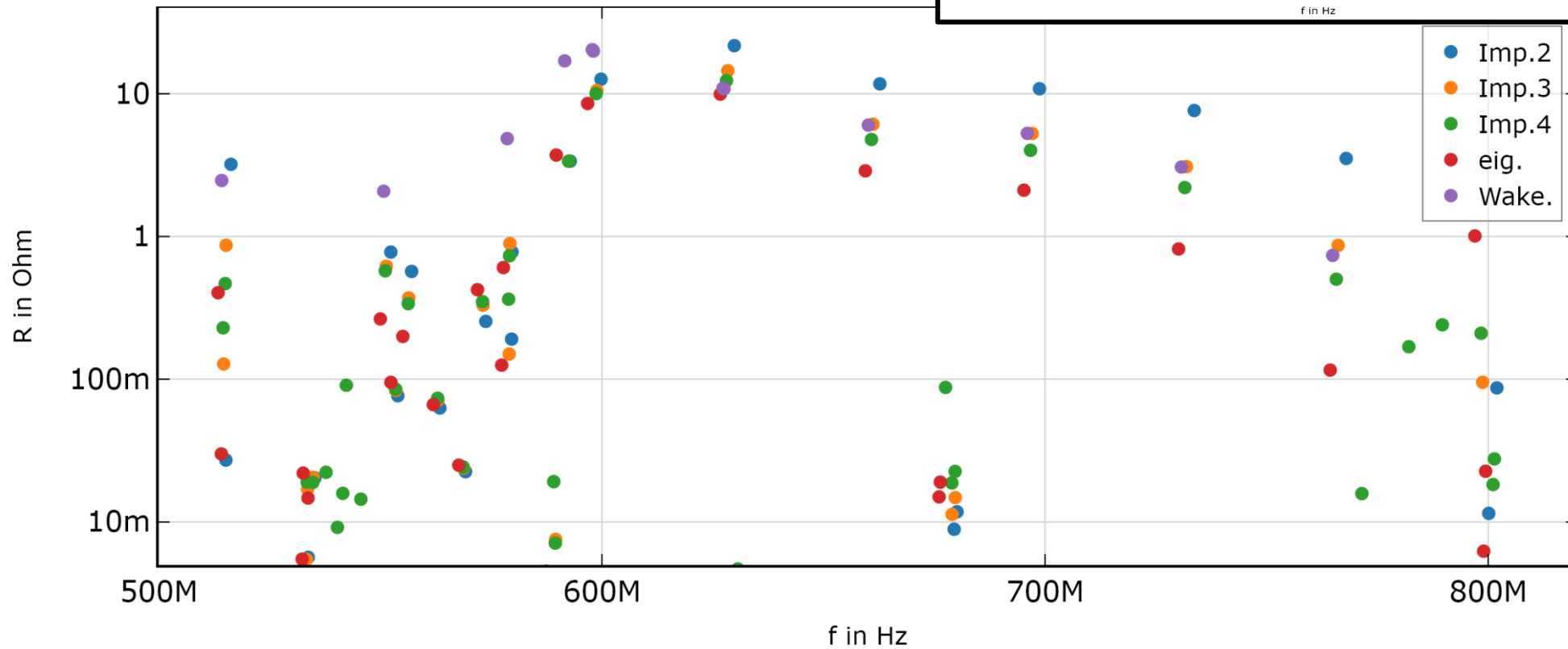
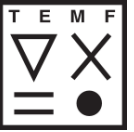
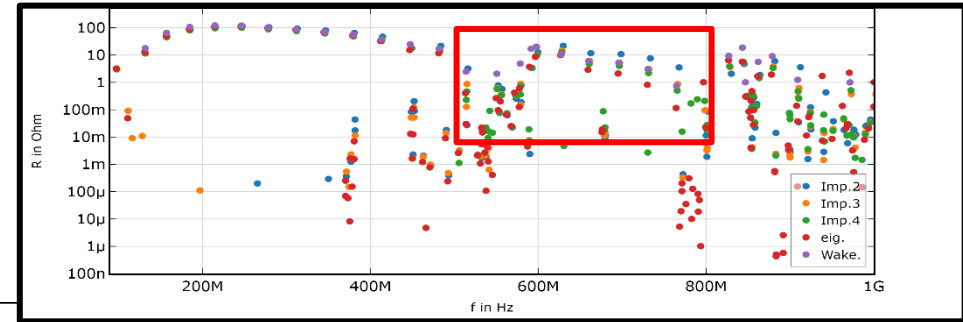
Shunt Impedances Comparison (PEC + steel)

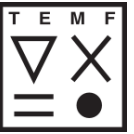




RESULTS

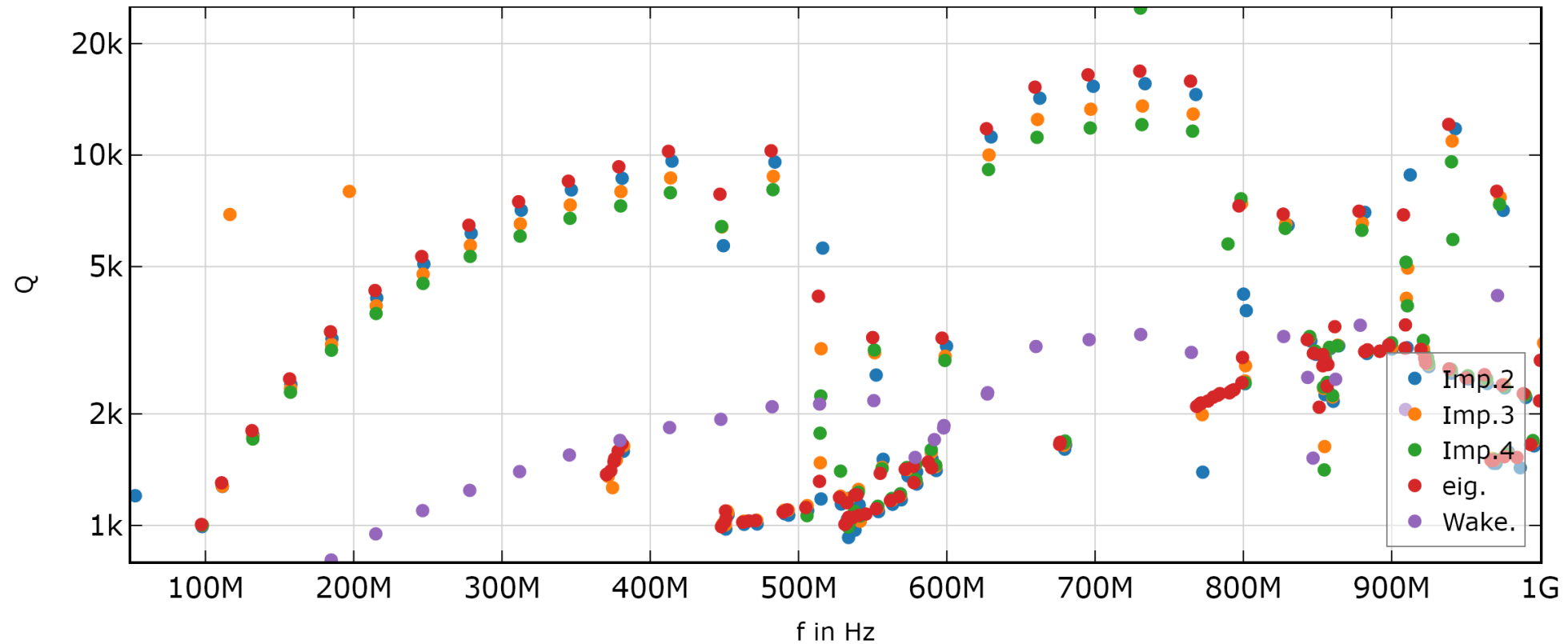
Shunt Impedances Comparison (PEC + steel)

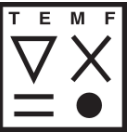




RESULTS

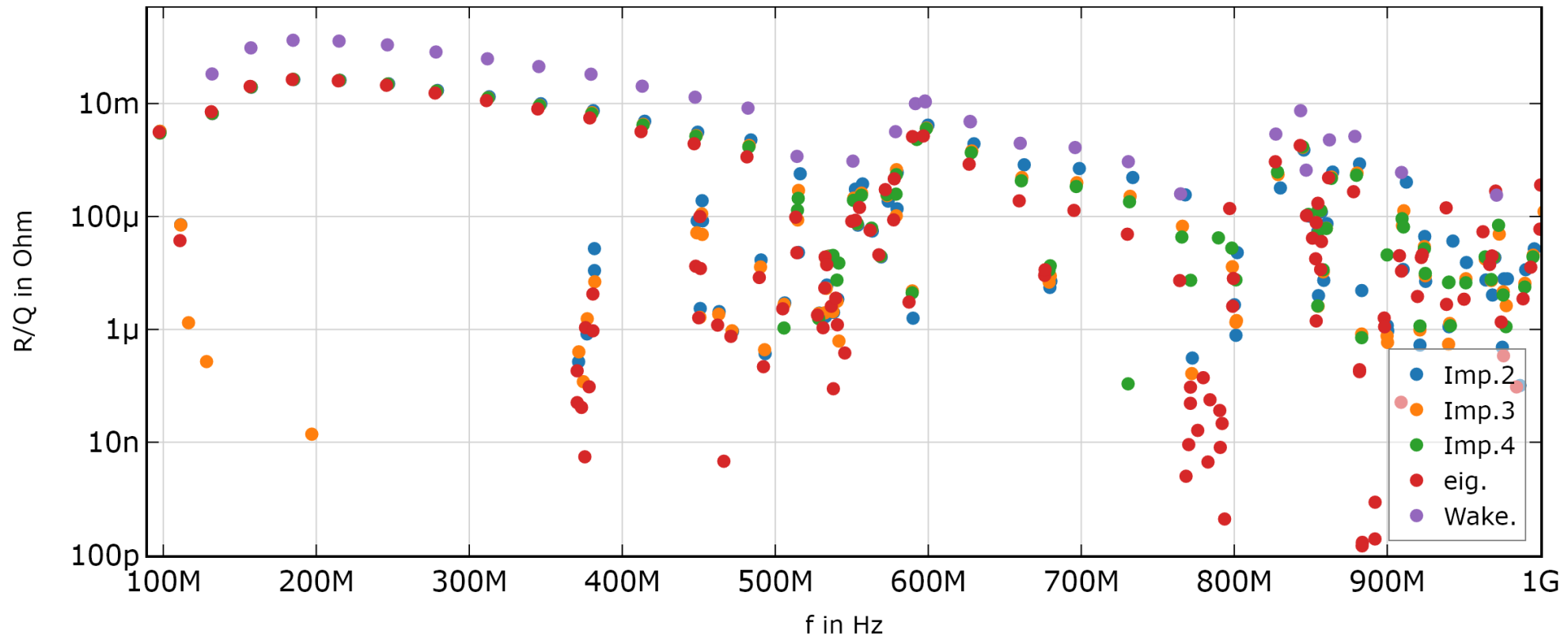
Q Comparison (PEC + steel)





RESULTS

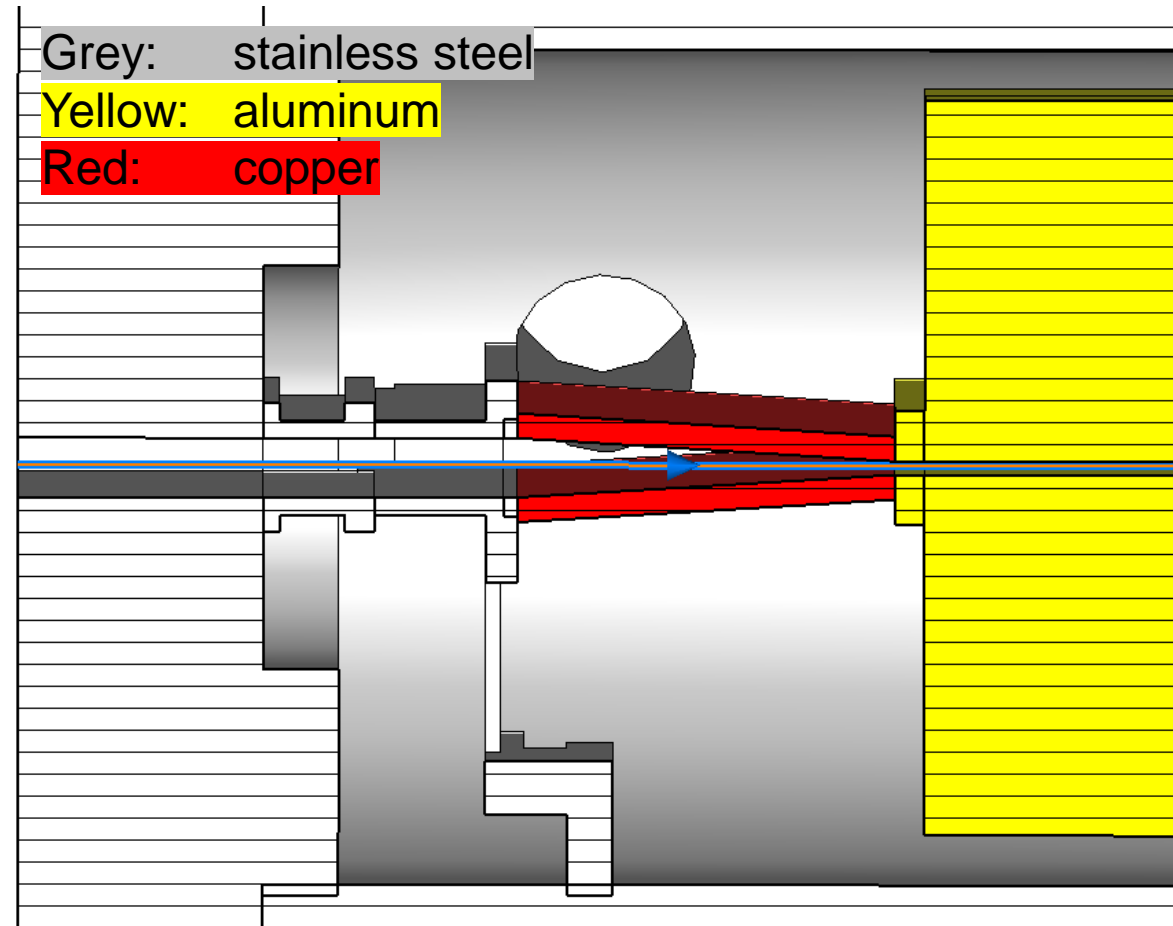
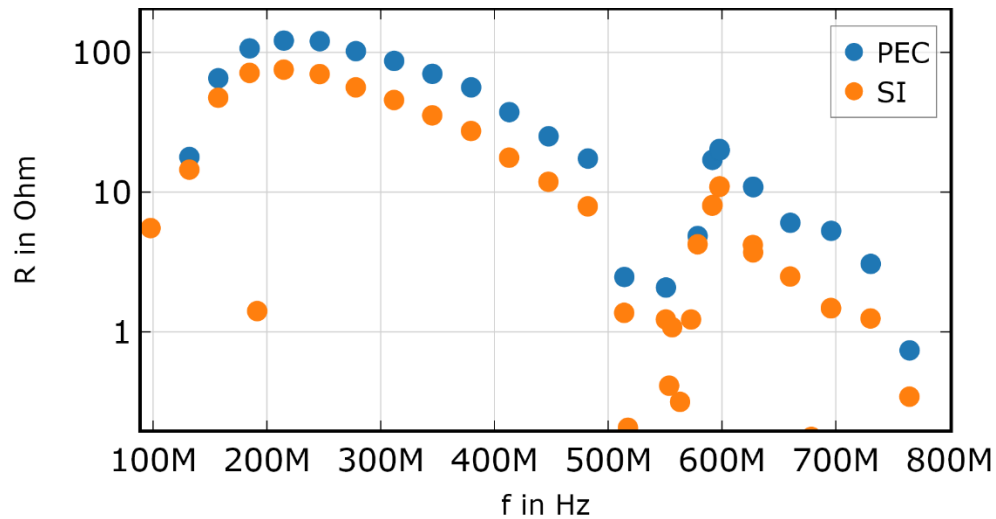
R/Q Comparison (PEC + steel)

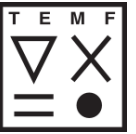


RESULTS

Effect of Materials on Zs

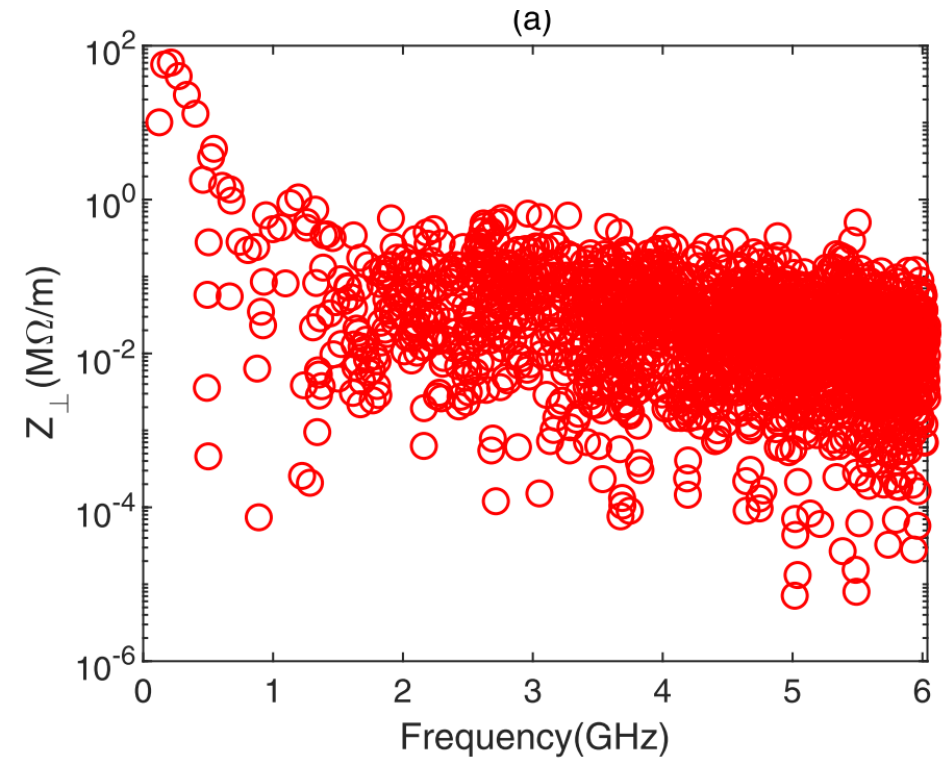
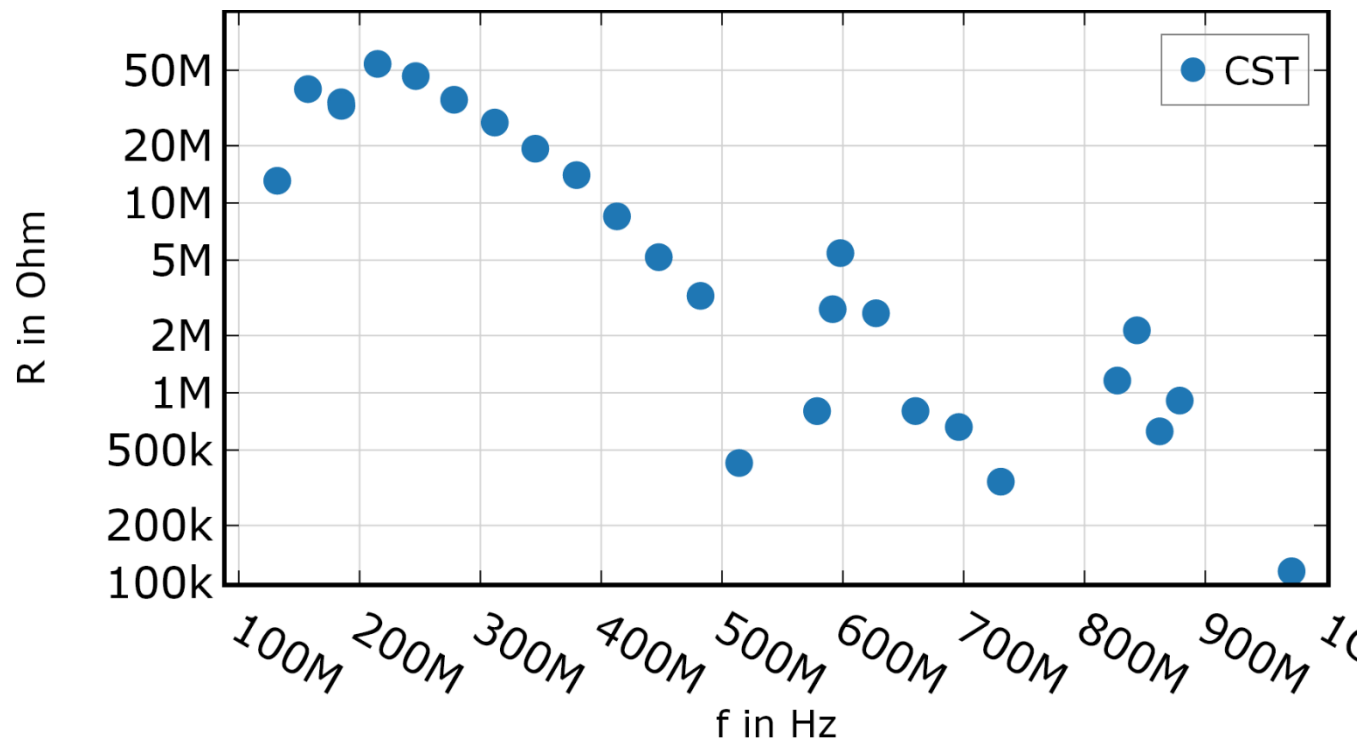
- CST Wakefield Solver





RESULTS

Transverse Shunt Impedances

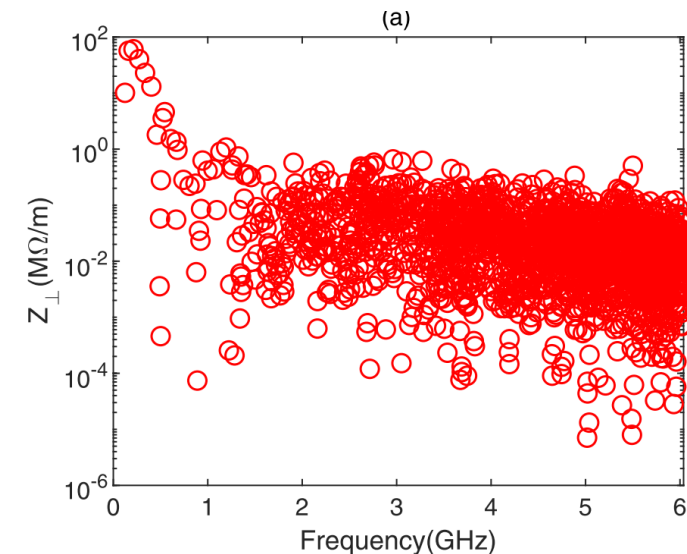
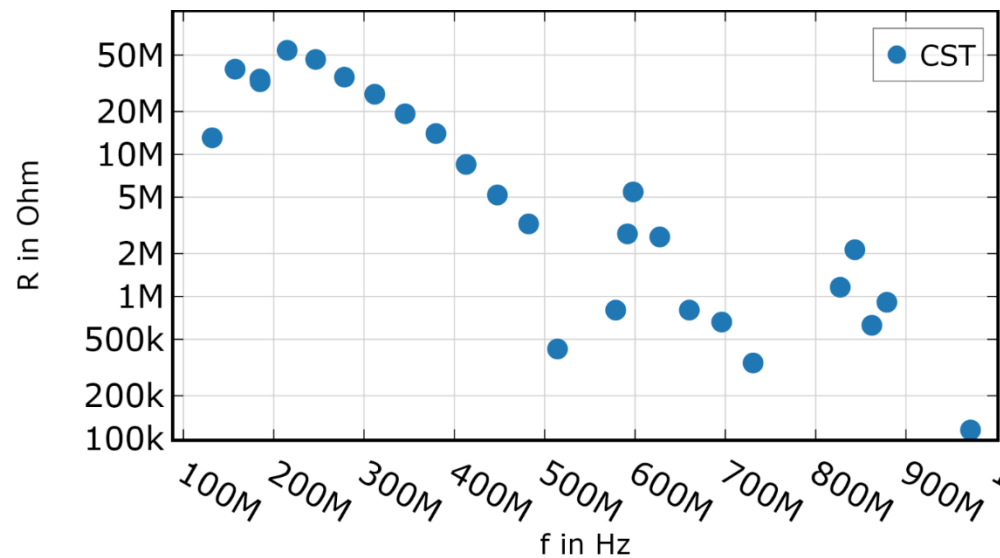


Tian et. Al., PRAB 2019

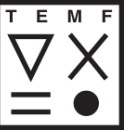
RESULTS

Transverse Shunt Impedances

- Similar mode pattern and magnitude
- Critical for stability: modes up to 300 MHz
- Damping scheme could be required (ferrites)

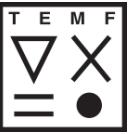


Tian et. Al., PRAB 2019



CONCLUSIONS

- Confident results up to 800MHz
 - f_0 (all three solver)
 - Shunt impedance (all three solver)
 - Q (eigenmode, impedance solvers)
 - R/Q (eigenmode, impedance solvers)
- Higher frequencies demand solver improvements
 - TD: geometry approximation, integration length
 - EM, FD: memory consumption
- Motivates development of advanced solvers



OUTLOOK

- Transverse shunt impedances
- Higher frequencies
- Geometry
 - Cooling
 - Flexible taper
 - Variable gap width
- Material properties
- Loss / cooling

